

**IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Donald E. Steiss et al.
APPLICATION NO.: 10/749,912
FILING DATE: December 30, 2003
TITLE: Instruction Memory Hierarchy for Embedded Processor
EXAMINER: Jacob Andrew Petranek
GROUP ART UNIT: 2183
ATTY. DKT. NO.: 22347-08261

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
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AMENDMENT B AND RESPONSE

Sir:

This amendment for the patent application identified above is in response to the Office Action dated July 25, 2006, which set a shortened statutory period for response that expires on October 25, 2006.

Kindly amend this application as indicated herein.